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design combinational logic circuits Æ Combinational logic circuits do not have an internal stored state, i.e., they have no memory. Consequently the output is solely a function of the current inputs. Æ Later, we will study circuits having a stored internal state, i.e., sequential logic circuits.

Digital Electronics Part I Æ“ Combinational and Sequential

Designer must how data flows between various registers of the design. Gate level: The module is implemented in terms of logic gates and interconnections between these gates. Designer should know the gate-level diagram of the design. Switch level: This is the lowest level of abstraction. The design is implemented using switches/transistors.

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Careful control during Structural modeling describes a digital logic networks in terms of fabrication is necessary to avoid this problem. the components that make up the system. Gate-level modeling is 34.

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